LOYOLA COLLEGE (AUTONOMOUS), CHENNAI – 600 034		
U.G. DEGREE EXAMINATION – ALLIED		
THIRD SEMESTER – NOVEMBER 2022		
UPH 3405 – DIGITAL ELECTRONICS		
LUCEAT LUX VESTIO		
Da	ate: 01-12-2022 Dept. No.	Max. : 100 Marks
Time: 09:00 AM - 12:00 NOON		
PART – A		
Answ	ver ALL questions	(10x 2 = 20 Marks)
1.	Convert the octal number $(75)_8$ to its decimal equivalent.	
2.	Convert $(1020)_{10}$ into a hexadecimal number.	
5. ₁	Define negative and positive logic.	
4. 5	Write any two lows of Poolean algebra	
5. 6	Whit is meant by sum of products method?	
0. 7.	What is a flin flon?	
8.	Draw the diagram of a T flip flop and give its truth table.	
9.	What is a register?	
10.	List the different types of counters.	
PART – B		
Ans	wer any FOUR questions	(4 x 7.5 = 30 Marks)
11	<ul> <li>a) Convert (11110)<sub>2</sub> into a decimal number.</li> <li>b) Convert (1486)<sub>10</sub> into an octal number.</li> </ul>	(2.5 marks) (2.5 marks)
	c) Convert $(600)_{10}$ into a hexadecimal number.	(2.5 marks)
12	With a neat diagram, explain the working of AND, OR, NOT and Ex-OR gat	æs.
13	Simplify using K map $Y = F(A, B, C, D) = \Sigma(0,1,3,5,7,9,11,12,13,14,15).$	
14	Explain the working of a clocked RS flip flop and give its truth table.	
15	With a neat diagram, describe the working of a shift right shift register.	
16	Discuss in detail the working of a synchronous up counter.	
PART – C		
Ansv	wer any FOUR questions	(4 x 12.5 = 50 Marks)
17	Explain NAND and NOR as universal gates with a neat diagram and draw th	eir truth tables.
18	What is a shift register? With a neat logic diagram explain the working of pa parallel-in parallel-out shift registers.	urallel-in serial- out and
19	<ul><li>a) Add 12 and 13 using binary number system.</li><li>b) Convert (456)<sub>10</sub> to octal number.</li></ul>	(3 marks) (3 marks)
	c) Convert the decimal number $(1542)_{10}$ to hexadecimal number.	(3 marks)
	d) Find the equivalent decimal number for the binary number $110011001_2$ .	(3.5 marks)
20	With a neat diagram and truth table explain the working of a JK flip flop.	
21	Draw the circuit diagram of Mod 4 and Mod 8 counters and explain its worki	ing.
22	Simplify using K map: $Y=F(A, B, C, D) = \Sigma(0, 1, 2, 4, 5, 10, 11, 14, 15)$ and draw the logic circuit for	

22 Simplify using K map: Y=F (A, B, C, D) = $\Sigma$  (0.1.2.4,5,10,11,14,15) and draw the logic circuit for the simplified expression.

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